



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/538,626	06/10/2005	Alexander Cornelis Geerlings	NL 021285	8943
24737	7590	12/28/2009	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS			AHMED, HAMDY S	
P.O. BOX 3001			ART UNIT	PAPER NUMBER
BRIARCLIFF MANOR, NY 10510			2186	
MAIL DATE	DELIVERY MODE			
12/28/2009	PAPER			

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Advisory Action Before the Filing of an Appeal Brief	Application No.	Applicant(s)
	10/538,626	GEERLINGS ET AL.
	Examiner	Art Unit
	HAMDY S. AHMED	2186

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 10 December 2009 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) The period for reply expires _____ months from the mailing date of the final rejection.
- b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because

- (a) They raise new issues that would require further consideration and/or search (see NOTE below);
- (b) They raise the issue of new matter (see NOTE below);
- (c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- (d) They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).

5. Applicant's reply has overcome the following rejection(s): _____.

6. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: _____.

Claim(s) objected to: _____.

Claim(s) rejected: 1-20.

Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).

9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).

10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.

12. Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). _____

13. Other: _____.

Continuation of 11. does NOT place the application in condition for allowance because: The applicant argues that Okazaki fails to teach the limitation in claim 1 of "measuring the signal performance of the storage device".

However, in column 3, lines 1-22, the reference teaches more than one control subsystem that monitors system performance (for example, lines 2-4 describe the control subsystem detecting the at least one data track and moving the pickup head assembly to maintain focus on the at least one data track; this subsystem also monitors and maintains reliable data transfer between pickup head assembly and the at least one data track (see lines 7-12). Data is transferred using signals, so monitoring and adjusting system performance with regard to data transfer is equivalent to measuring and adjusting signal performance.

The applicant argues further that Okazaki fails to teach the limitation in claim 1 of "when a measured performance of the storage device decreases below a pre-determined level taking action to reduce the influence of vibrations generated by the source of vibrations." However, in column 3, lines 13-23, Okazaki discloses "disabling the tracking between the pickup head assembly and the at least one data track prior to detecting movement of the pickup head, for determining whether the movement of the pickup head is within at least one predetermined limit, and for changing relative speed of movement between the pickup head and the removable storage media to bring the movement of the pickup head within the at least one predetermined limit."

The applicant argues further that Okazaki fails to disclose the limitation in claim 1 of "wherein the signal performance of the storage device includes at least one of access time of the storage device, data access rate, and data storage rate." However, in column 4, lines 46-67, and column 5, lines 1-4, Okazaki describes the role of output signals from the photodiodes A-F as representing information stored on the disk; thus by monitoring and adjusting system vibration, the operation of the photodiodes is maintained, which results in the data access rate being monitored and maintained in the case of reading the track.

The applicant argues that Kimura fails to teach a circuit for operating the storage device. However, it is known that a storage device is accessed using a circuit, with storage device itself being comprised of circuitry.

/Matt Kim/
SPE, AU2186